FIG. 1

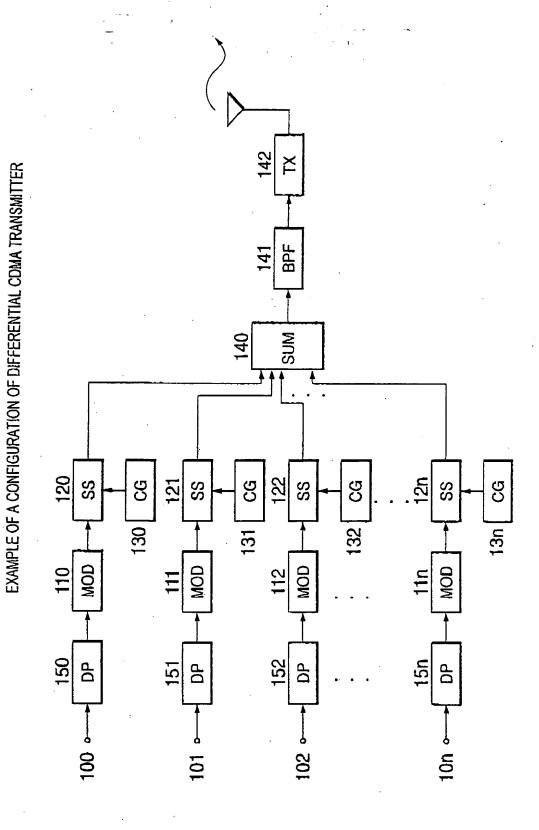


FIG.2

EXAMPLE OF A DETAILED CONFIGURATION OF DIFFERENTIAL CODING CIRCUIT (DP)

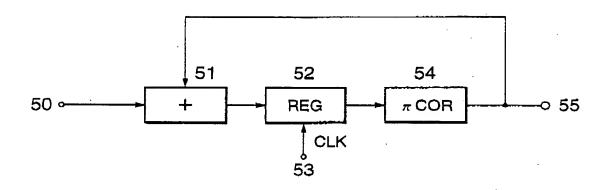


FIG. 3

INPUT/OUTPUT CHARACTERISTICS OF $\,\pi\,$ CORRECTOR ($\,\pi\,$ COR)

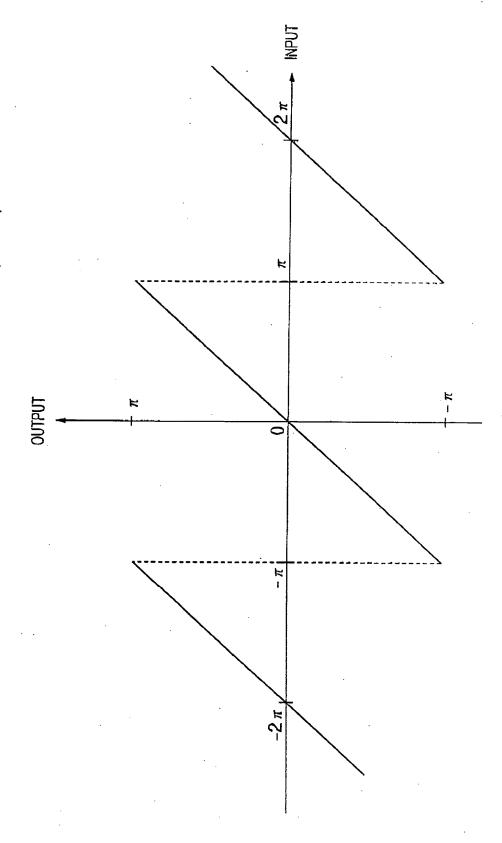


FIG. 4

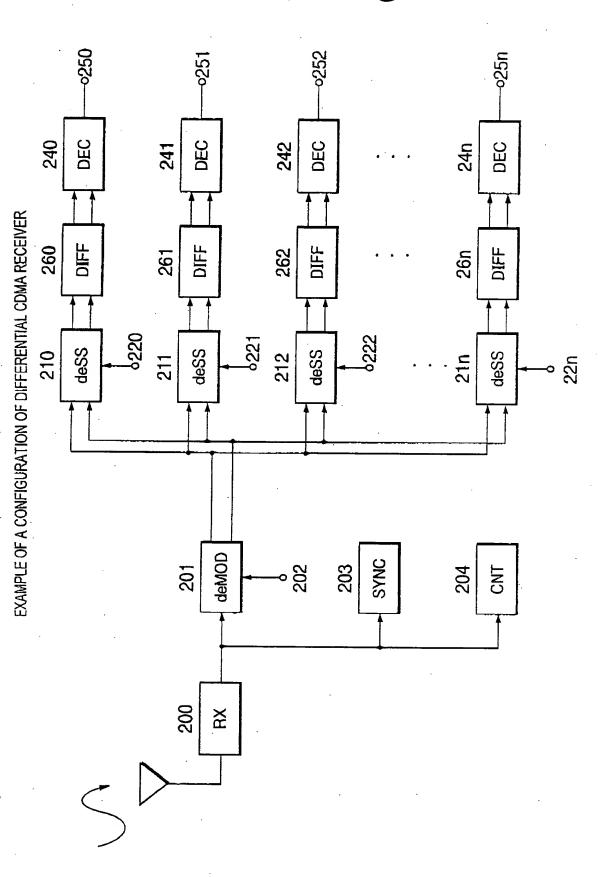


FIG.5

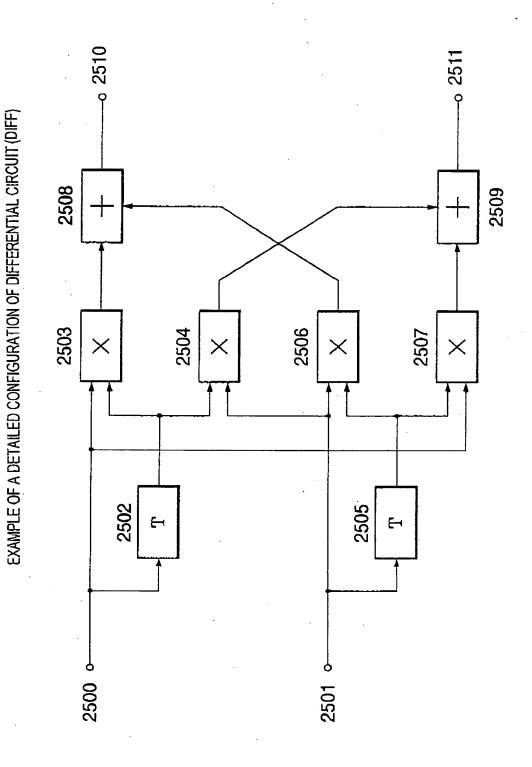


FIG. 6

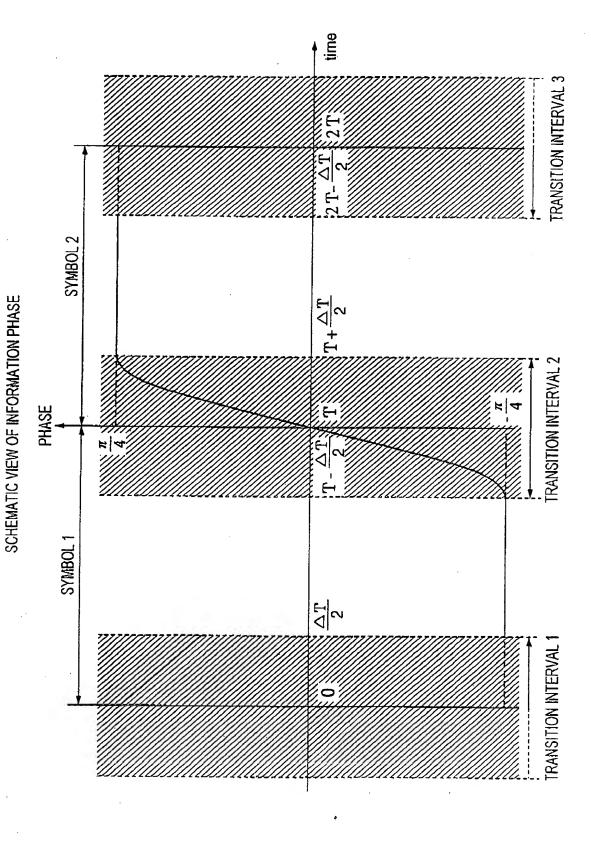


FIG.

THE WINDS OF

EXAMPLE OF A DETAILED CONFIGURATION OF PHASE CONTINUOUS DIFFERENTIAL CODING CIRCUIT (DP-CP)

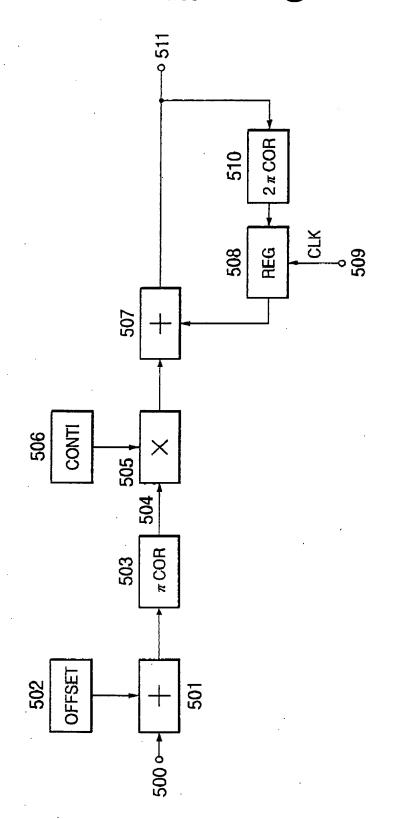


FIG.8

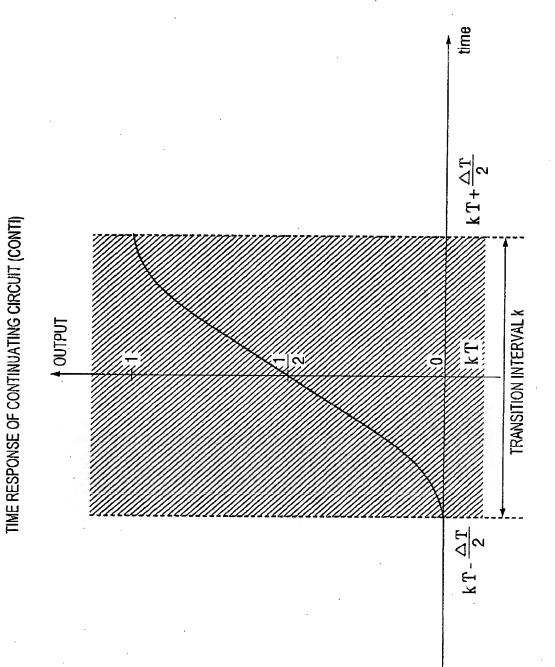


FIG. 9

EXAMPLE OF A DETAILED CONFIGURATION OF PHASE CONTINUATING CIRCUIT (CP)

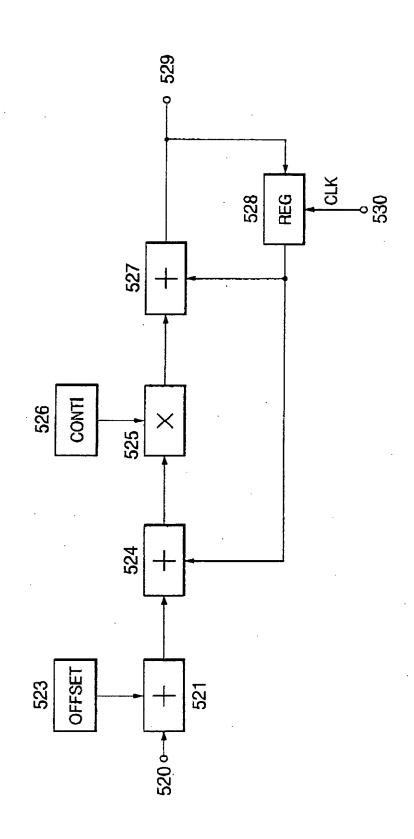


FIG. 10

SCHEMATIC VIEW OF PRIMARY MODULATED WAVEFORM

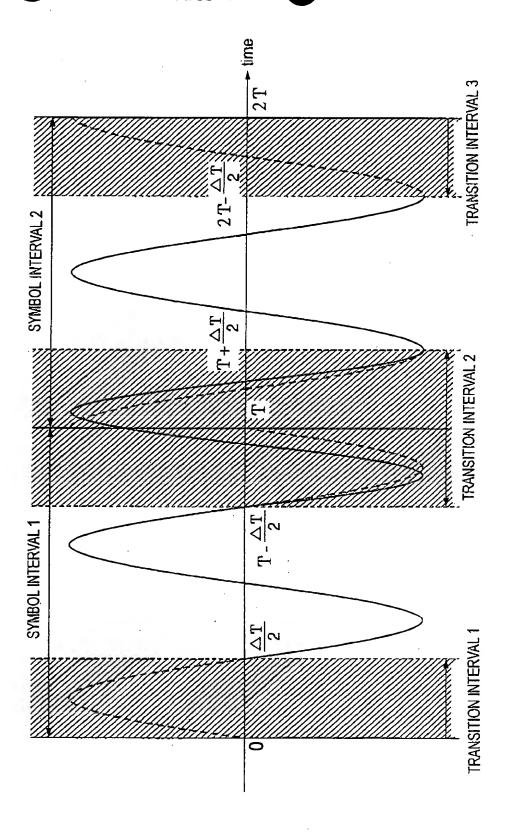


FIG. 1.

SCHEMATIC VIEW OF SPREAD CODE SEQUENCE WAVEFORM

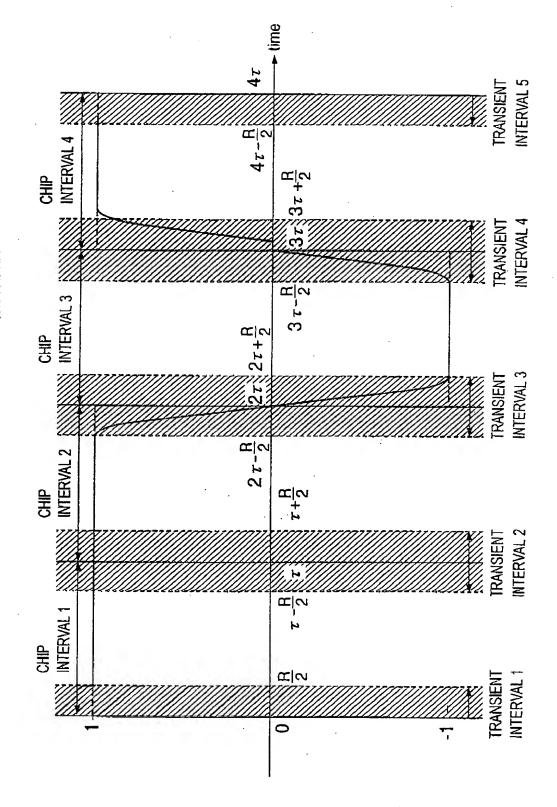


FIG. 12

EXAMPLE OF A DETAILED CONFIGURATION OF SPREAD CODE SEQUENCE WAVEFORM CONTINUATING CIRCUIT (CODE-CS)

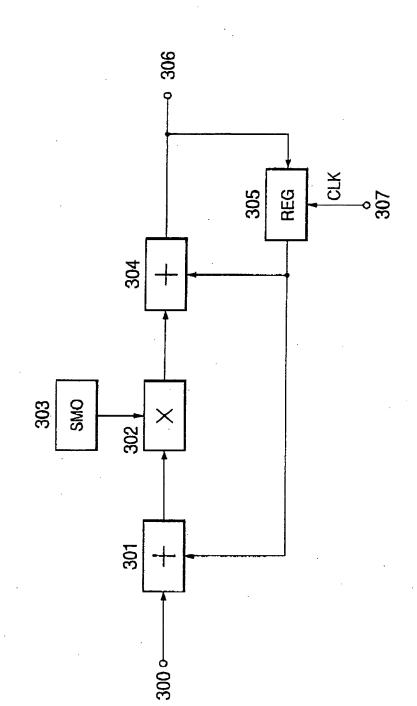


FIG. 13

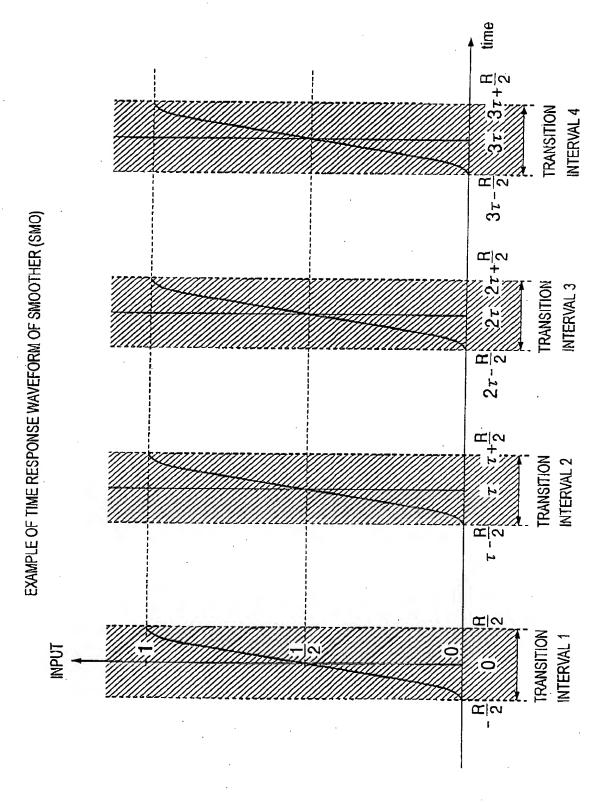


FIG. 14

EXAMPLE OF VIRTUAL SEGMENT INTERLEAVE STRUCTURE

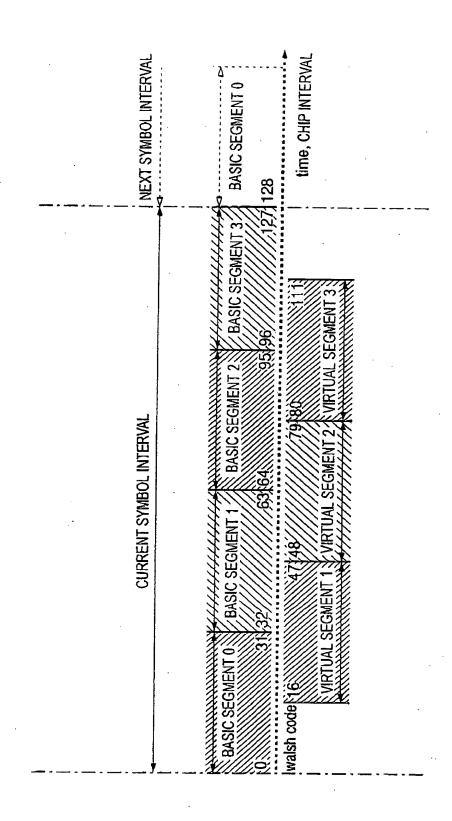
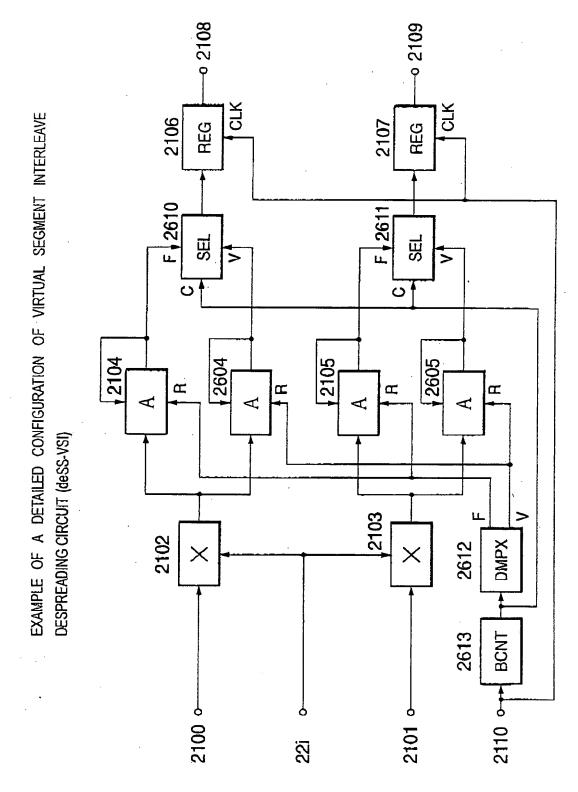


FIG. 15

with the state



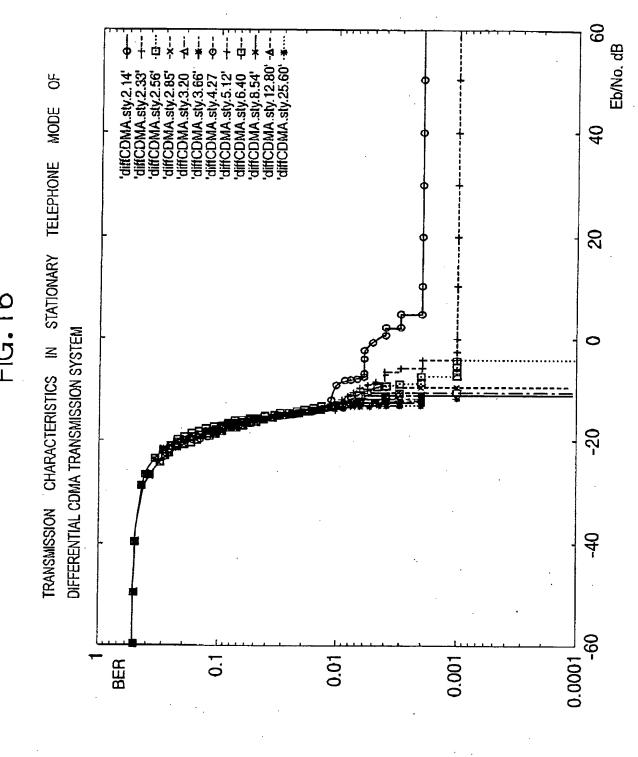
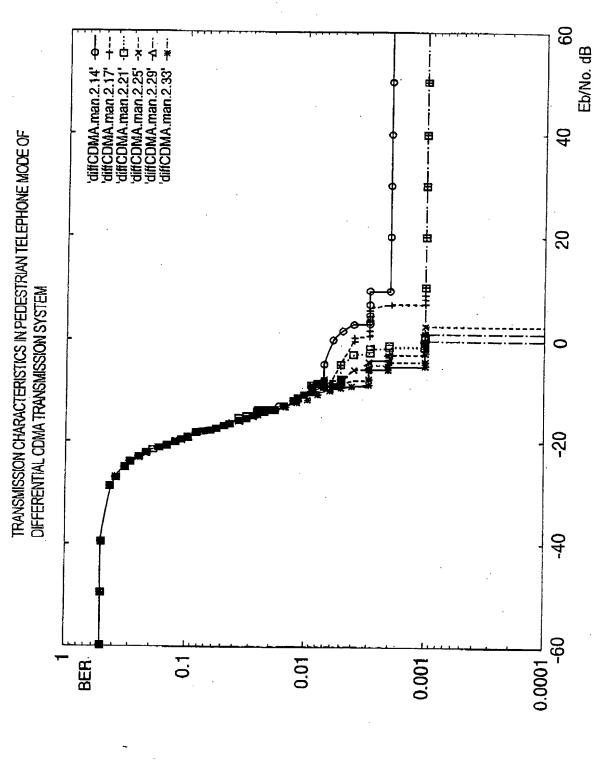


FIG. 17



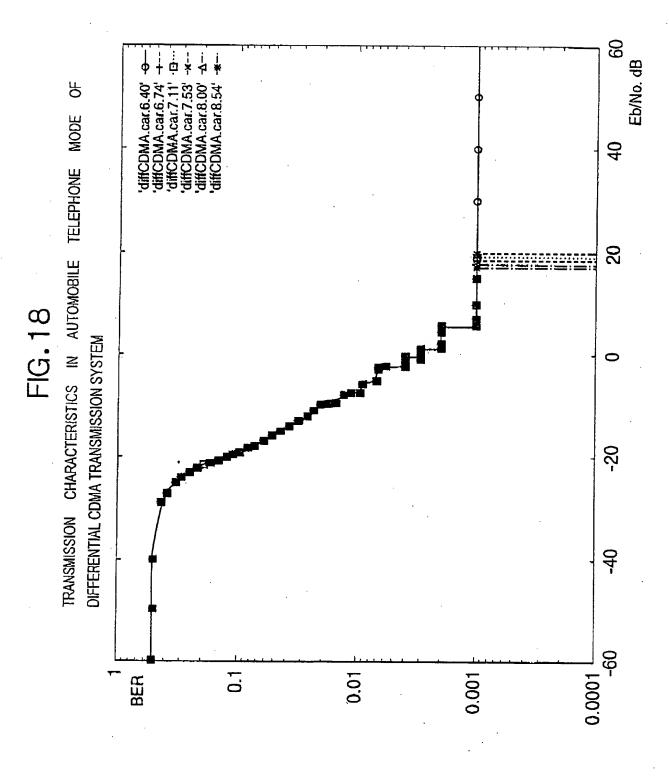


FIG. 19

SCHEMATIC POWER SPECTRUM

power

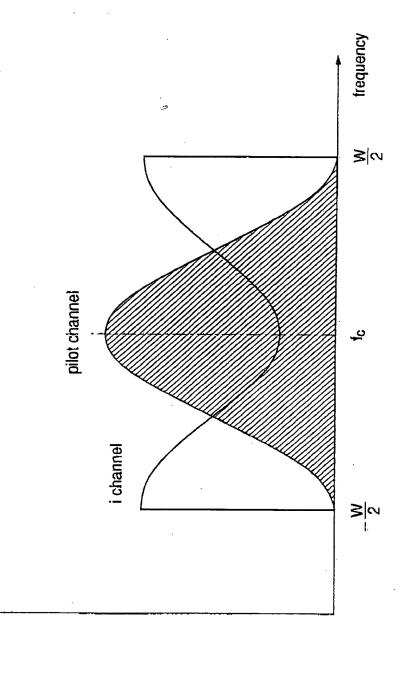
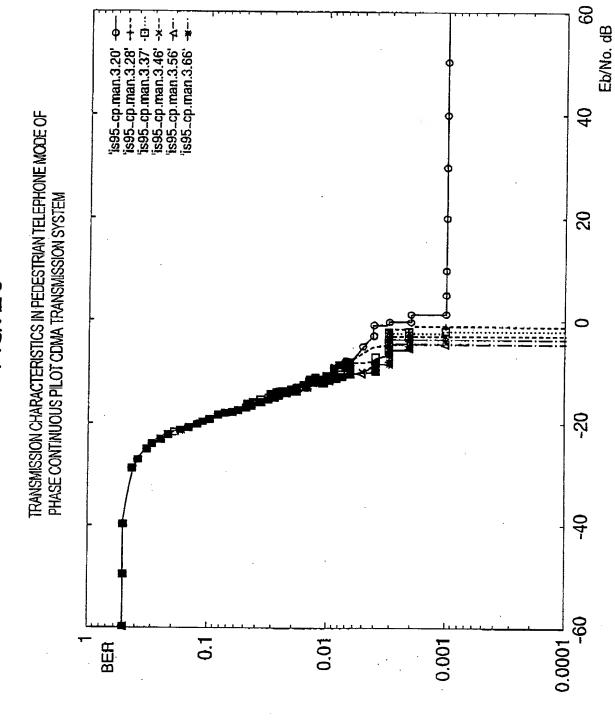


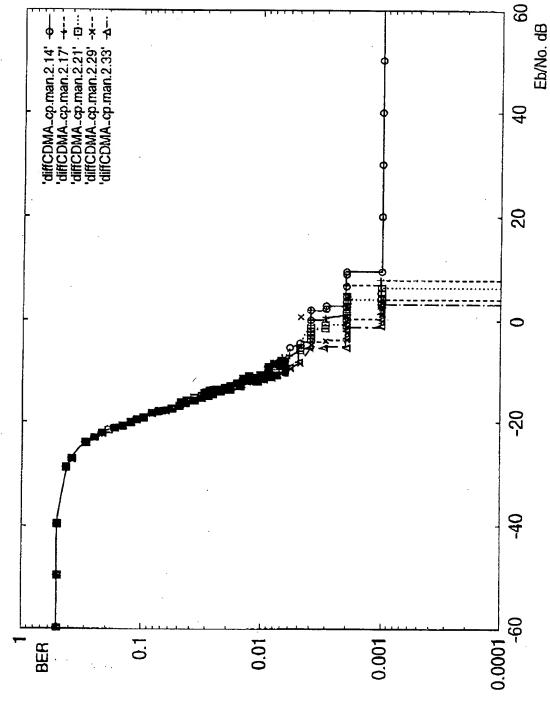
FIG. 20



15 T 30 T

FIG. 21





9

Eb/No. dB 'diffCDMA_cs.man.2.14' -diffCDMA_cs.man.2.17' -diffCDMA_cs.man.2.21' -diffCDMA_cs.man.2.29' -diffCDMA_cs.man.2.29' -diffCDMA_cs.man.2.33' -40 TRANSMISSION CHARACTERISTICS IN PEDESTRIAN TELEPHONE MODE OF CHIP CONTINUOUS DIFFERENTIAL COMA TRANSMISSION SYSTEM 20 FIG. 22 -50 49 0.001 0.1 BER

FIG. 23

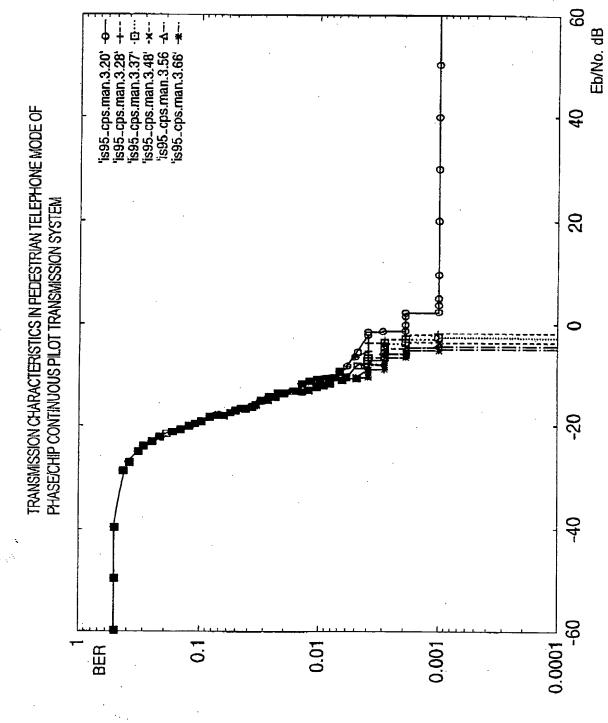


FIG. 24

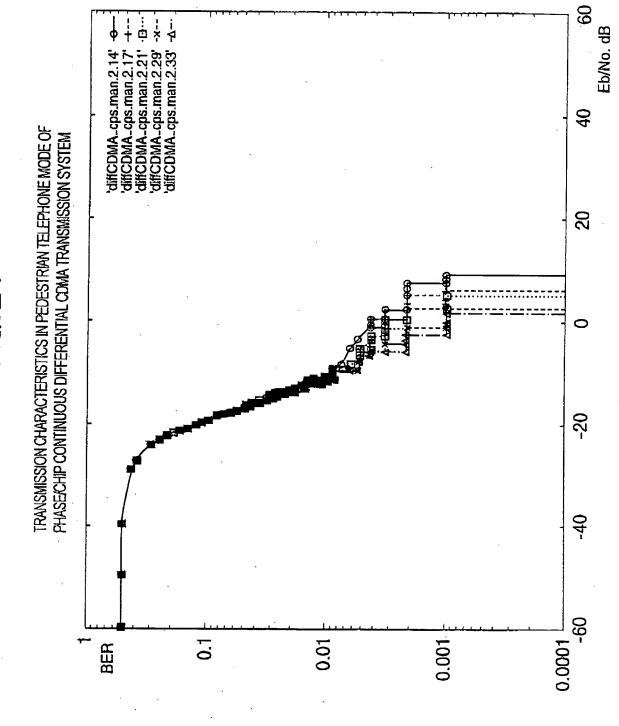


FIG. 25

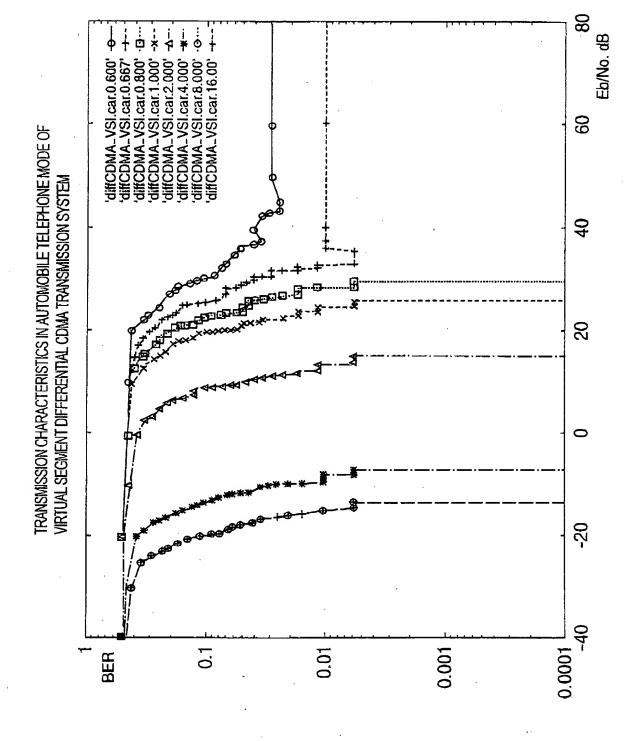


FIG. 26

EXAMPLE OF A CONFIGURATION OF THE CONVENTIONAL COMA TRANSMITTER

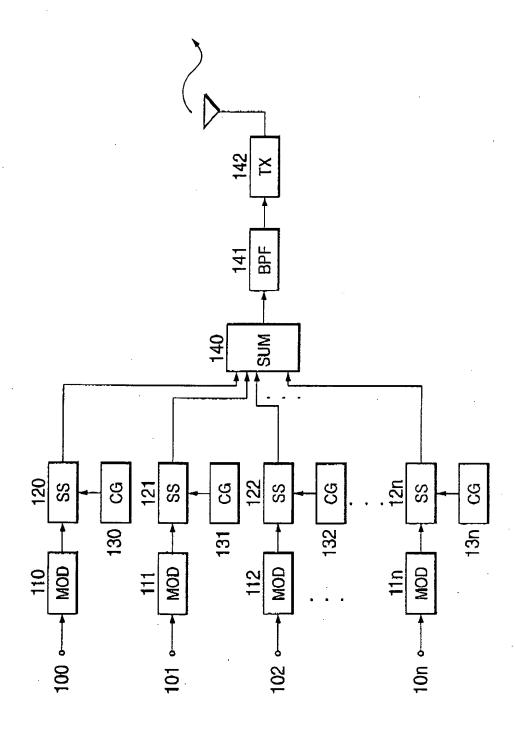


FIG.27

SCHEMATIC VIEW OF PRIMARY MODULATED WAVE AND SYMBOL STRUCTURE

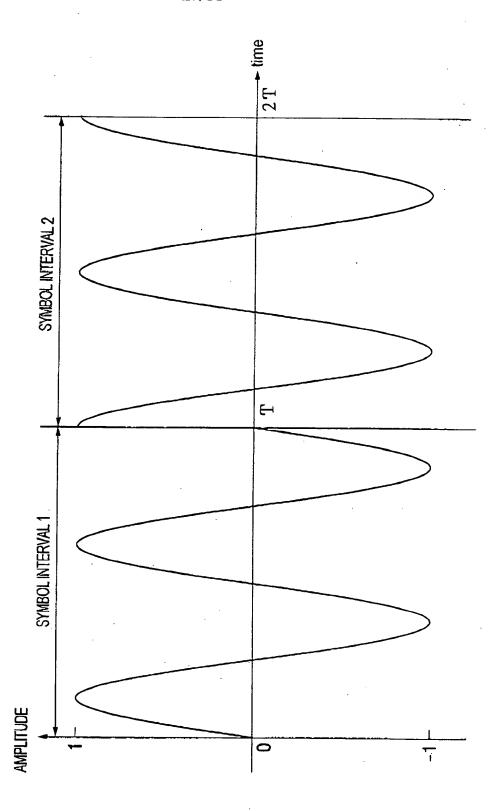


FIG.28

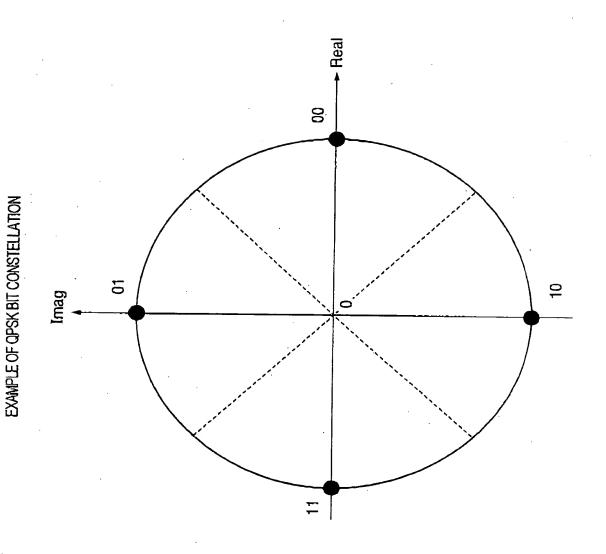


FIG. 29

THE VOW BEAUTIST OF O

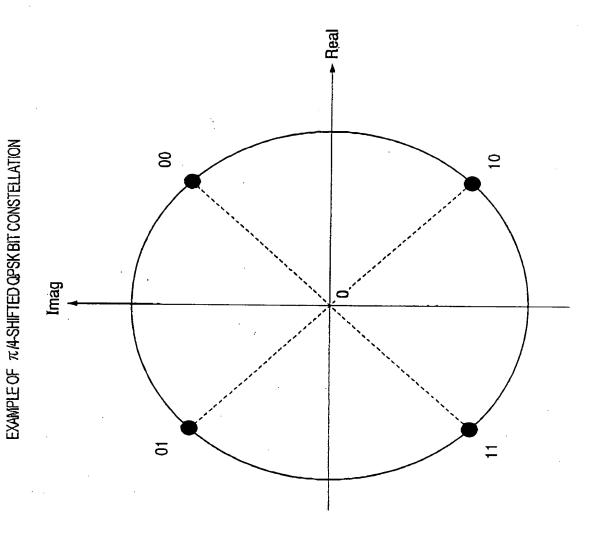
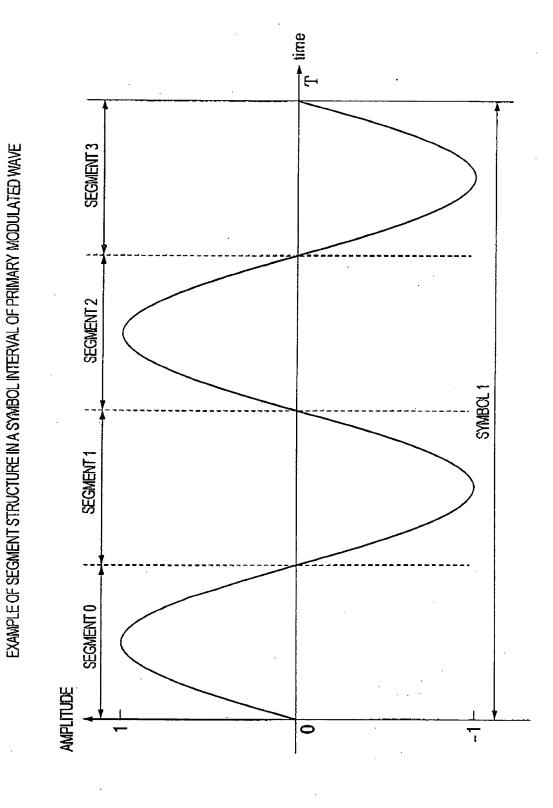


FIG. 30





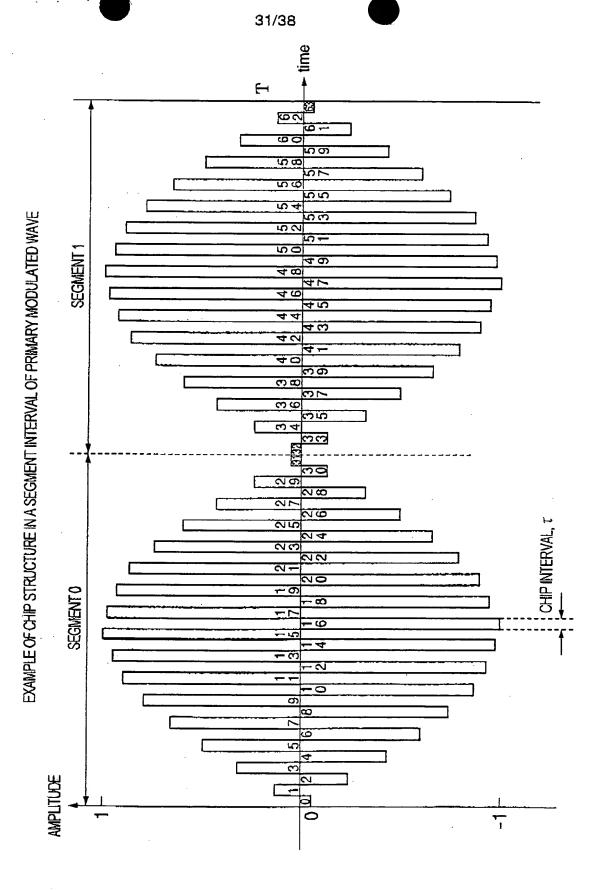


FIG. 32

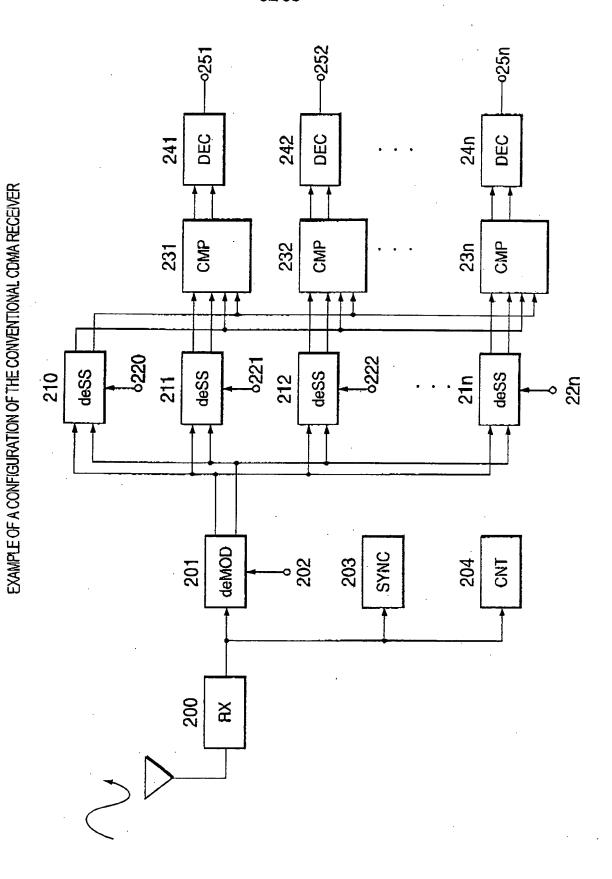


FIG. 33

EXAMPLE OF A DETAILED CONFIGURATION OF DEMODULATOR CIRCUIT (de MOD)

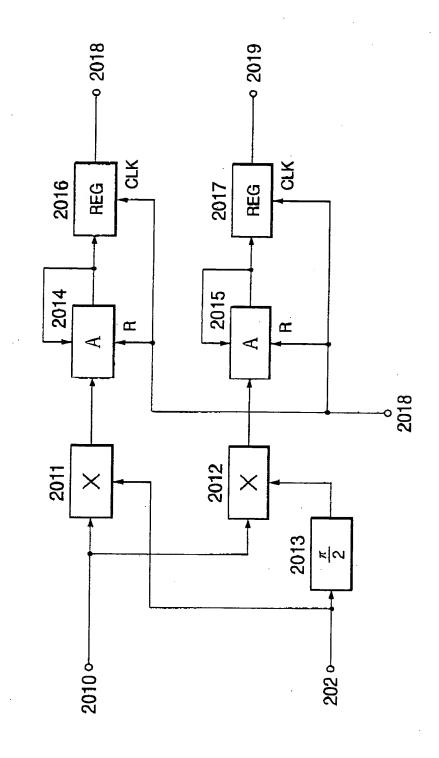


FIG.34

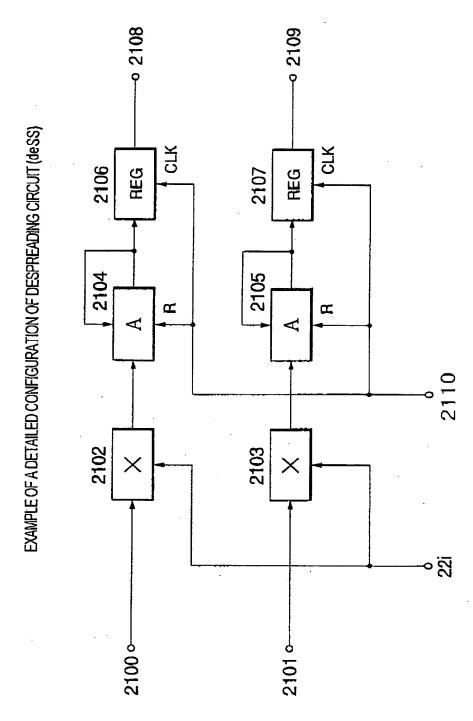


FIG. 35

EXAMPLE OF A DETAILED CONFIGURATION OF PHASE CORRECTION CIRCUIT (CMP)

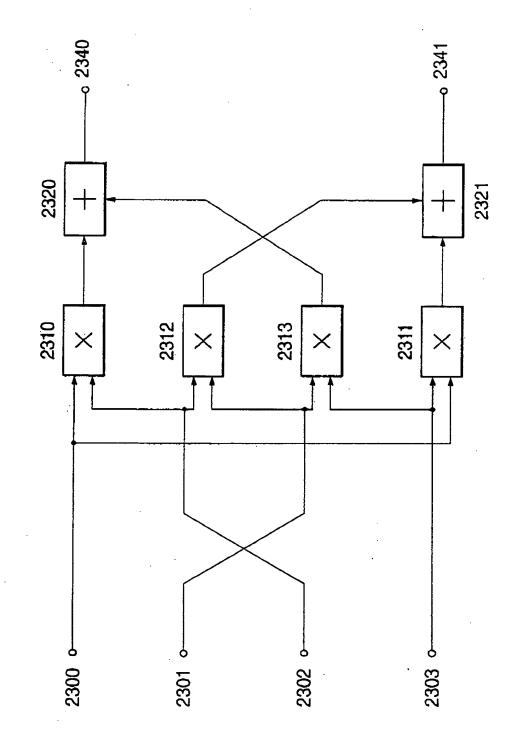


FIG.36

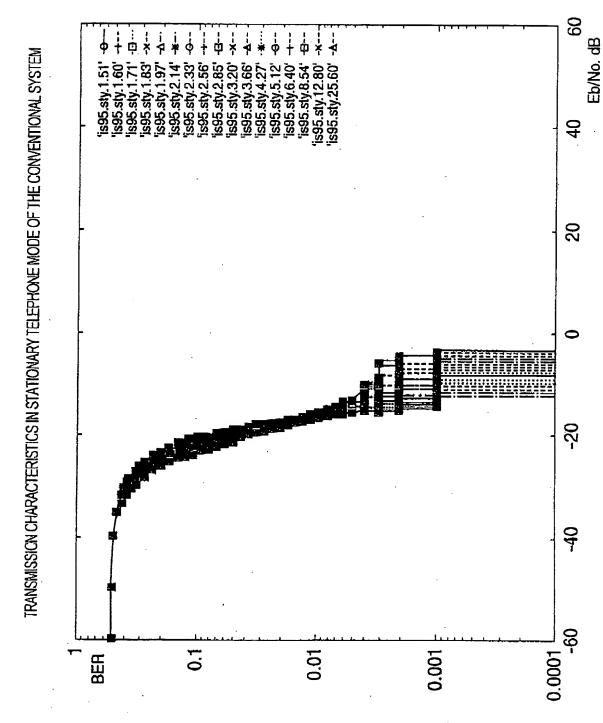


FIG. 37

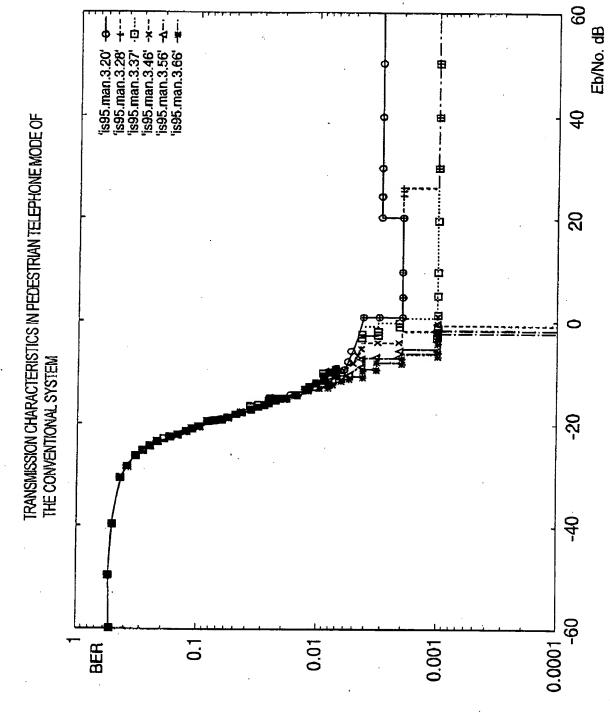


FIG. 38

